



SGM8581

Single-Supply, Single Rail-to-Rail I/O Precision Operational Amplifier

GENERAL DESCRIPTION

The SGM8581 is a single, precision operational amplifier which can operate from 2.5V to 5.5V single supply. The device provides rail-to-rail input and output operation.

The SGM8581 offers a low offset voltage less than 100 μ V and an ultra-low bias current of 15pA. The combination of characteristics makes the SGM8581 a good choice for temperature measurements, pressure and position sensors, strain gauge amplifiers and medical instrumentation, or any other 2.5V to 5.5V applications requiring precision and long-term stability.

The SGM8581 is available in Green SOT-23-5, SOIC-8 and MSOP-8 packages. It is specified over the extended industrial temperature range (-40 °C to +125°C).

FEATURES

- **Low Offset Voltage: 100 μ V (MAX)**
- **Ultra-Low Input Bias Current: 15pA**
- **Large-Signal Voltage Gain: 145dB (TYP) at 5V**
- **PSRR: 120dB (TYP)**
- **CMRR: 90dB (TYP)**
- **Overload Recovery Time: 70 μ s (at $V_S = 5V$)**
- **Rail-to-Rail Input and Output**
- **Supply Voltage Range: 2.5V to 5.5V**
- **Low Supply Current: 445 μ A at 5V**
- **No External Capacitors Required**
- **-40°C to +125°C Operating Temperature Range**
- **Available in Green SOT-23-5, SOIC-8 and MSOP-8 Packages**

APPLICATIONS

Pressure Sensors
Temperature Measurements
Precision Current Sensing
Electronic Scales
Strain Gauge Amplifiers
Handheld Test Equipment
Thermocouple Amplifiers
Medical Instrumentation

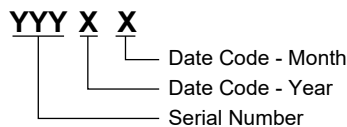
PACKAGE/ORDERING INFORMATION

MODEL	PACKAGE DESCRIPTION	SPECIFIED TEMPERATURE RANGE	ORDERING NUMBER	PACKAGE MARKING	PACKING OPTION
SGM8581	SOT-23-5	-40°C to +125°C	SGM8581XN5G/TR	S0BXX	Tape and Reel, 3000
	SOIC-8	-40°C to +125°C	SGM8581XS8G/TR	SGM8581XS8 XXXXX	Tape and Reel, 2500
	MSOP-8	-40°C to +125°C	SGM8581XMS8G/TR	SGM8581 XMS8 XXXXX	Tape and Reel, 3000

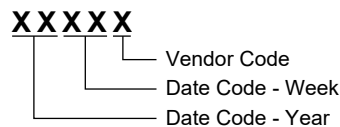
MARKING INFORMATION

NOTE: XX = Date Code. XXXXX = Date Code and Vendor Code.

SOT-23-5



SOIC-8/MSOP-8



Green (RoHS & HSF): SG Micro Corp defines "Green" to mean Pb-Free (RoHS compatible) and free of halogen substances. If you have additional comments or questions, please contact your SGMICRO representative directly.

ABSOLUTE MAXIMUM RATINGS

- Supply Voltage 6V
- Input Voltage Range -V_S to (+V_S) + 0.1V
- Differential Input Voltage Range -5V to 5V
- Junction Temperature +150°C
- Storage Temperature Range..... -65°C to +150°C
- Lead Temperature (Soldering, 10s).....+260°C
- ESD Susceptibility
- HBM (SOT-23-5)..... 6000V
- HBM (SOIC-8) 7000V
- HBM (MSOP-8)..... 6000V
- MM..... 400V

RECOMMENDED OPERATING CONDITIONS

- Operating Temperature Range -40°C to +125°C

OVERSTRESS CAUTION

Stresses beyond those listed in Absolute Maximum Ratings may cause permanent damage to the device. Exposure to absolute maximum rating conditions for extended periods

may affect reliability. Functional operation of the device at any conditions beyond those indicated in the Recommended Operating Conditions section is not implied.

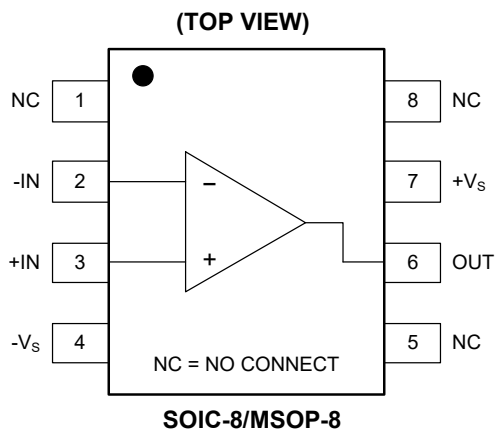
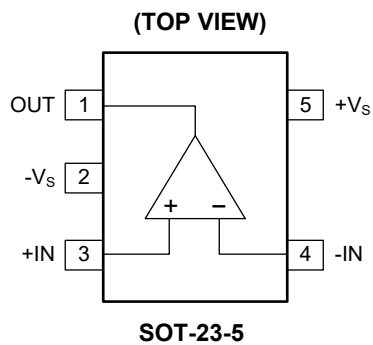
ESD SENSITIVITY CAUTION

This integrated circuit can be damaged if ESD protections are not considered carefully. SGMICRO recommends that all integrated circuits be handled with appropriate precautions. Failure to observe proper handling and installation procedures can cause damage. ESD damage can range from subtle performance degradation to complete device failure. Precision integrated circuits may be more susceptible to damage because even small parametric changes could cause the device not to meet the published specifications.

DISCLAIMER

SG Micro Corp reserves the right to make any change in circuit design, or specifications without prior notice.

PIN CONFIGURATIONS



ELECTRICAL CHARACTERISTICS(V_S = 5V, V_{CM} = 2.5V, V_{OUT} = 2.5V, Full = -40°C to +125°C, typical values are at T_A = +25°C, unless otherwise noted.)

PARAMETER	CONDITIONS	TEMP	MIN	TYP	MAX	UNITS
Input Characteristics						
Input Offset Voltage (V _{OS})		+25°C		25	100	μV
		Full			106	
Input Offset Voltage Drift (ΔV _{OS} /ΔT)		Full		100		nV/°C
Input Bias Current (I _B)		+25°C		15		pA
Input Offset Current (I _{OS})		+25°C		10		pA
Input Voltage Range		+25°C	0		5	V
Common Mode Rejection Ratio ⁽¹⁾ (CMRR)	V _{CM} = 0V to 5V	+25°C	80	90		dB
		Full	62			
Large-Signal Voltage Gain (A _{VO})	R _L = 10kΩ, V _{OUT} = 0.3V to 4.7V	+25°C	95	145		dB
		Full	91			
Output Characteristics						
Output Voltage High (V _{OH})	R _L = 100kΩ to -V _S	+25°C	4.99	4.998		V
		Full	4.979			
	R _L = 10kΩ to -V _S	+25°C	4.98	4.994		
		Full	4.96			
Output Voltage Low (V _{OL})	R _L = 100kΩ to +V _S	+25°C		2	10	mV
		Full			11	
	R _L = 10kΩ to +V _S	+25°C		6	15	
		Full			18	
Short-Circuit Limit (I _{SC})	V _{OUT} = 2.5V, R _L = 10Ω to GND	+25°C	40	45		mA
		Full	26			
Power Supply						
Power Supply Rejection Ratio ⁽¹⁾ (PSRR)	V _S = 2.5V to 5.5V	+25°C	90	120		dB
		Full	73			
Quiescent Current (I _Q)	V _{OUT} = V _S /2	+25°C		445	700	μA
		Full			845	
Dynamic Performance						
Gain-Bandwidth Product (GBP)	A _V = +100	+25°C		1.5		MHz
Slew Rate (SR)	A _V = +1, R _L = 10kΩ, 2V output step	+25°C		0.75		V/μs
Overload Recovery Time	A _V = -100, R _L = 10kΩ, V _{IN} = 200mV (RET to GND)	+25°C		0.07		ms
Noise						
Input Voltage Noise (e _{n P-P})	0.1Hz to 10Hz	+25°C		0.85		μV _{P-P}
Input Voltage Noise Density (e _n)	f = 1kHz	+25°C		47.5		nV/√Hz

NOTE: 1. PSRR and CMRR are affected by the matching between external gain-setting resistor ratios.

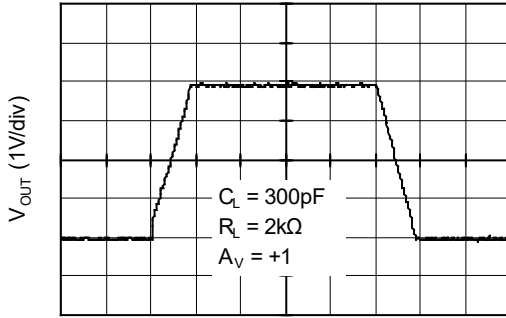
ELECTRICAL CHARACTERISTICS (continued)(V_S = 2.5V, V_{CM} = 1.25V, V_{OUT} = 1.25V, Full = -40°C to +125°C, typical values are at T_A = +25°C, unless otherwise noted.)

PARAMETER	CONDITIONS	TEMP	MIN	TYP	MAX	UNITS
Input Characteristics						
Input Offset Voltage (V _{OS})		+25°C		25	100	μV
		Full			127	
Input Offset Voltage Drift (ΔV _{OS} /ΔT)		Full		150		nV/°C
Input Bias Current (I _B)		+25°C		15		pA
Input Offset Current (I _{OS})		+25°C		10		pA
Input Voltage Range		+25°C	0		2.5	V
Common Mode Rejection Ratio ⁽¹⁾ (CMRR)	V _{CM} = 0V to 2.5V	+25°C	75	90		dB
		Full	61			
Large-Signal Voltage Gain (A _{VO})	R _L = 10kΩ, V _{OUT} = 0.3V to 2.4V	+25°C	95	140		dB
		Full	91			
Output Characteristics						
Output Voltage High (V _{OH})	R _L = 100kΩ to -V _S	+25°C	2.49	2.498		V
		Full	2.473			
	R _L = 10kΩ to -V _S	+25°C	2.48	2.497		
		Full	2.46			
Output Voltage Low (V _{OL})	R _L = 100kΩ to +V _S	+25°C		1	10	mV
		Full			11	
	R _L = 10kΩ to +V _S	+25°C		3	15	
		Full			16	
Short-Circuit Limit (I _{SC})	V _{OUT} = 1.25V, R _L = 10Ω to GND	+25°C	20	27		mA
		Full	14			
Power Supply						
Power Supply Rejection Ratio ⁽¹⁾ (PSRR)	V _S = 2.5V to 5.5V	+25°C	90	120		dB
		Full	73			
Quiescent Current (I _Q)	V _{OUT} = V _S /2	+25°C		440	700	μA
		Full			786	
Dynamic Performance						
Gain-Bandwidth Product (GBP)	A _V = +100	+25°C		1.45		MHz
Slew Rate (SR)	A _V = +1, R _L = 10kΩ, 2V output step	+25°C		0.75		V/μs
Overload Recovery Time	A _V = -100, R _L = 10kΩ, V _{IN} = 200mV (RET to GND)	+25°C		0.04		ms
Noise						
Input Voltage Noise (e _{n P-P})	0.1Hz to 10Hz	+25°C		0.9		μV _{P-P}
Input Voltage Noise Density (e _n)	f = 1kHz	+25°C		77		nV/√Hz

NOTE: 1. PSRR and CMRR are affected by the matching between external gain-setting resistor ratios.

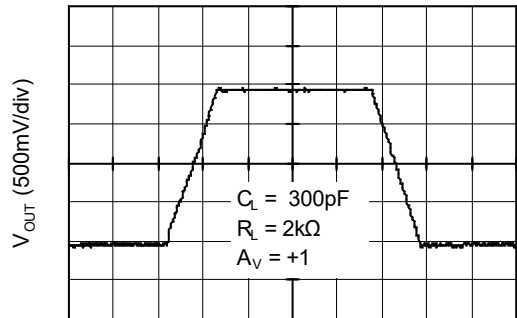
TYPICAL PERFORMANCE CHARACTERISTICS

Large Signal Transient Response at +5V



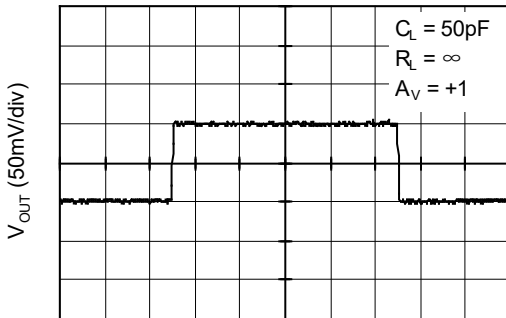
Time (5 μ s/div)

Large Signal Transient Response at +2.5V



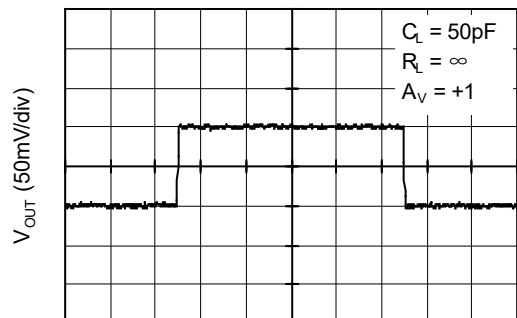
Time (2 μ s/div)

Small Signal Transient Response at +5V



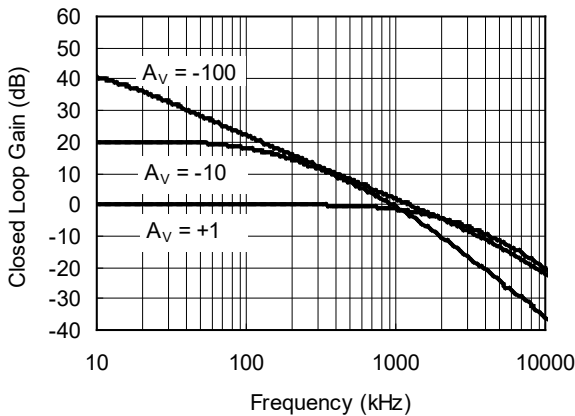
Time (5 μ s/div)

Small Signal Transient Response at +2.5V

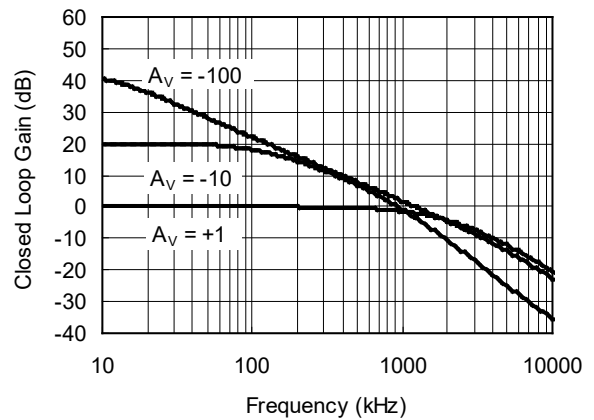


Time (5 μ s/div)

Closed Loop Gain vs. Frequency at +5V

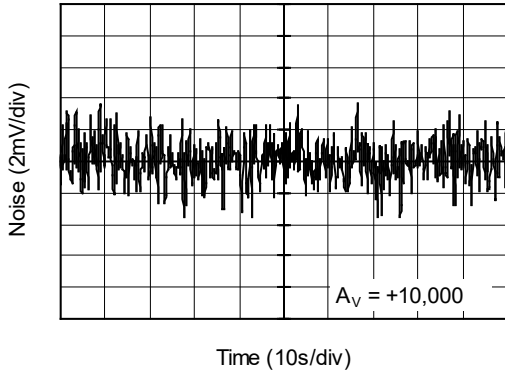


Closed Loop Gain vs. Frequency at +2.5V

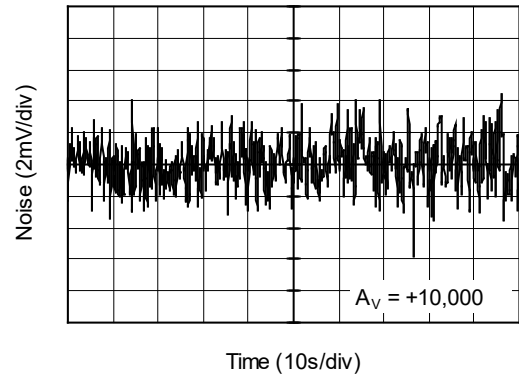


TYPICAL PERFORMANCE CHARACTERISTICS (continued)

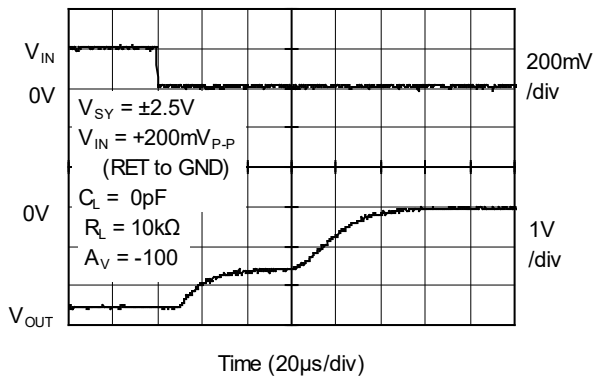
0.1Hz to 10Hz Noise at +5V



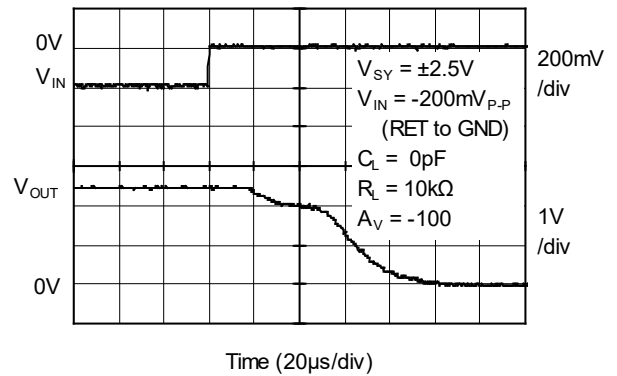
0.1Hz to 10Hz Noise at +2.5V



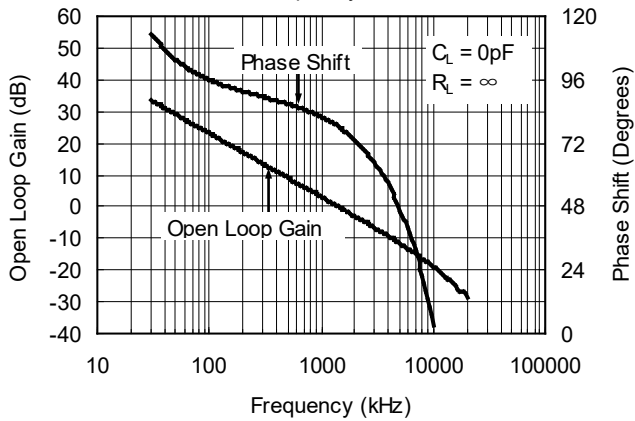
Negative Overtolerance Recovery



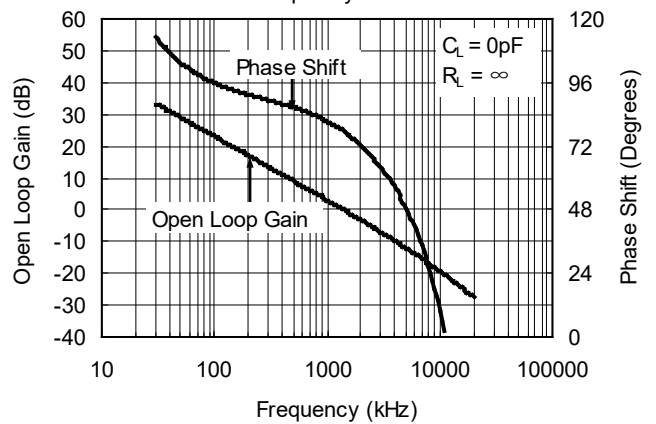
Positive Overtolerance Recovery



Open Loop Gain, Phase Shift vs. Frequency at +5V

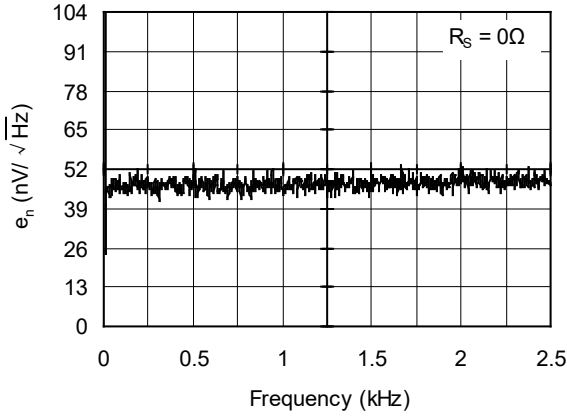


Open Loop Gain, Phase Shift vs. Frequency at +2.5V

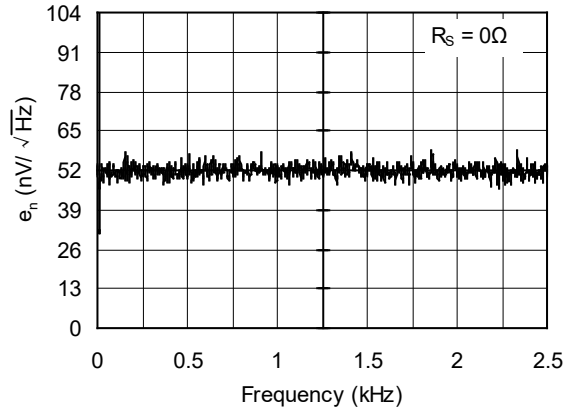


TYPICAL PERFORMANCE CHARACTERISTICS (continued)

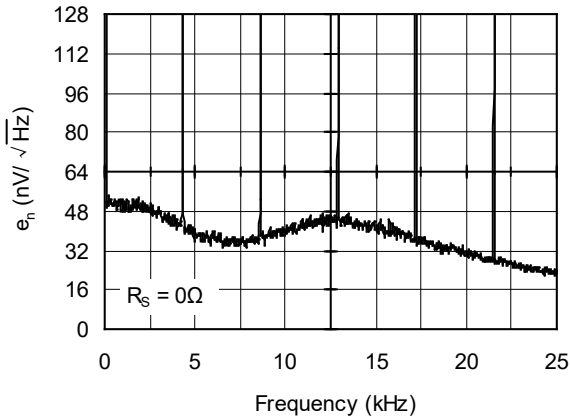
Voltage Noise Density at +5V
from 0.1Hz to 2.5kHz



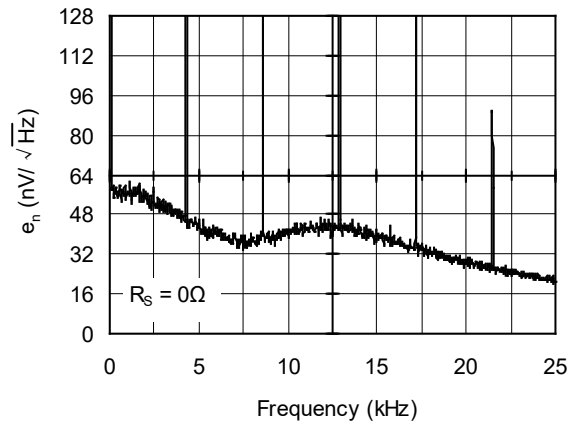
Voltage Noise Density at +2.5V
from 0.1Hz to 2.5kHz



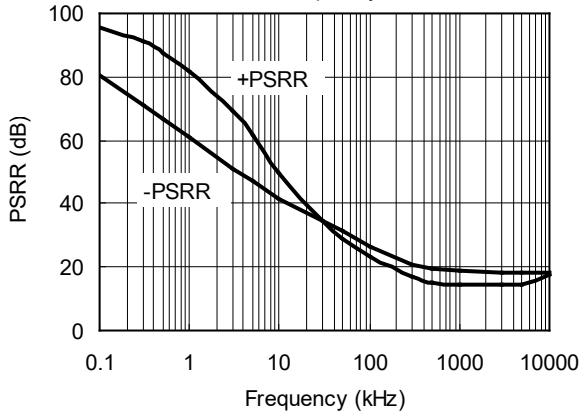
Voltage Noise Density at +5V
from 0.1Hz to 25kHz



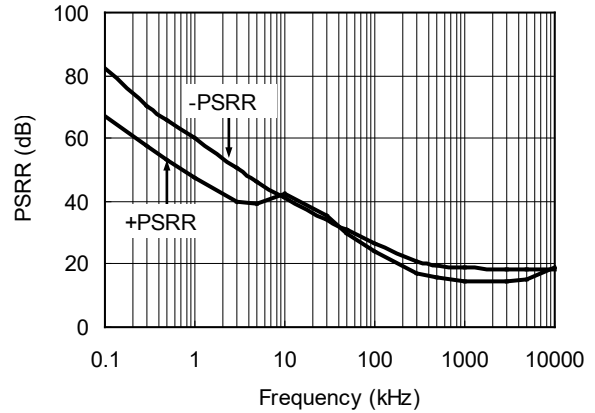
Voltage Noise Density at +2.5V
from 0.1Hz to 25kHz



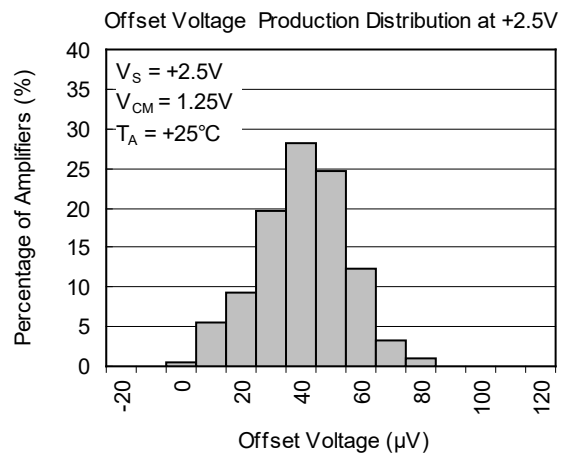
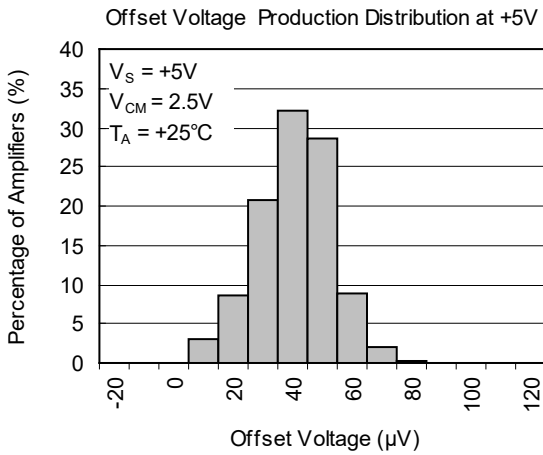
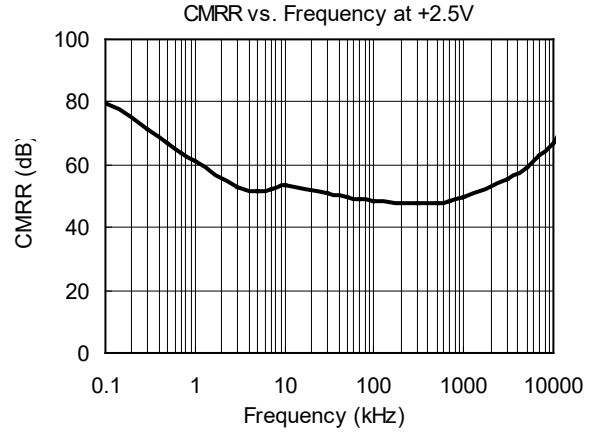
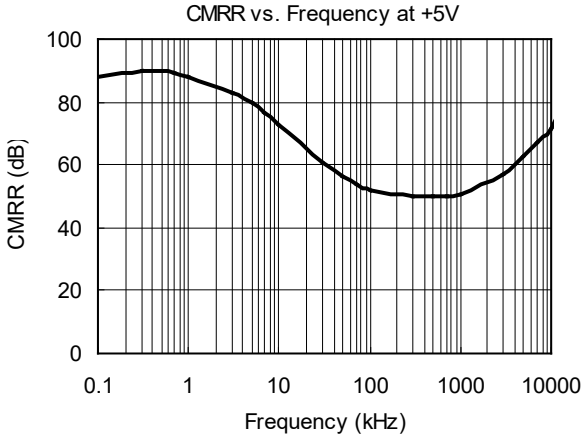
PSRR vs. Frequency at ±2.5V



PSRR vs. Frequency at ±1.25V



TYPICAL PERFORMANCE CHARACTERISTICS (continued)



REVISION HISTORY

NOTE: Page numbers for previous revisions may differ from page numbers in the current version.

JANUARY 2013 – REV.A.2 to REV.A.3	Page
Added Tape and Reel Information section	14, 15
DECEMBER 2011 – REV.A.1 to REV.A.2	Page
Updated Electrical Characteristics section	3, 4
Updated Typical Performance Characteristics section	7
Updated Package Outline Dimensions section	9~11
MAY 2011 – REV.A to REV.A.1	Page
Changed packages' name	All
Changes from Original (MARCH 2010) to REV.A	Page
Changed from product preview to production data	All