SGM48760XXDM
Single SPST Analog Switch

GENERAL DESCRIPTION

The SGM48760XXDM is a single SPST analog switch which is designed for 1.65V to 5.5V V_{CC} operation. The device can support up to 5.5V bidirectional signal transmission. It can be flexibly applied to digital and analog signal processing systems.

The SGM48760XXDM is available in a Green XTDFN-1×1-6L package.

FEATURES

- **Supply Voltage Range: 1.65V to 5.5V**
- **Low On-Resistance: 4.5Ω (TYP) at V_{CC} = 4.5V**
- **With No V_{CC} Connected in the System, the Control Pin can Accept Voltages up to 5.5V**
- **Propagation Delay Time, t_{PD}:**
  0.65ns (TYP) at V_{CC} = 3.3V
- **Rail-to-Rail Input and Output Operation**
- **-40℃ to +125℃ Operating Temperature Range**
- **Available in a Green XTDFN-1×1-6L Package**

APPLICATIONS

ADC and DAC
Computer Peripherals
Portable Equipment
Sample-and-Hold Circuits

LOGIC DIAGRAM

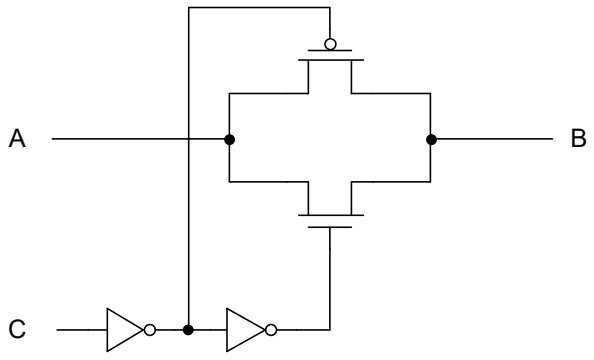


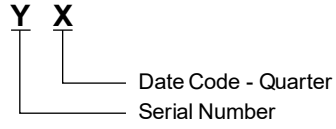
Figure 1. Logic Diagram

PACKAGE/ORDERING INFORMATION

MODEL	PACKAGE DESCRIPTION	SPECIFIED TEMPERATURE RANGE	ORDERING NUMBER	PACKAGE MARKING	PACKING OPTION
SGM48760XXDM	XTDFN-1×1-6L	-40℃ to +125℃	SGM48760XXDM6G/TR	NX	Tape and Reel, 5000

MARKING INFORMATION

NOTE: X = Date Code.



Green (RoHS & HSF): SG Micro Corp defines "Green" to mean Pb-Free (RoHS compatible) and free of halogen substances. If you have additional comments or questions, please contact your SGMICRO representative directly.

ABSOLUTE MAXIMUM RATINGS

Supply Voltage, V_{CC} (1) ..... -0.5V to 6.5V
Control Input Voltage, V_{IN} (1) (2) ..... -0.5V to 6.5V
Switch I/O Voltage, $V_{I/O}$ (1) (2) (3) ..... -0.5V to (V_{CC} + 0.5V)
Control Input Clamp Current, I_{IK}, V_{IN} < 0V .....-50mA
I/O Port Diode Current, $I_{I/OK}$, $V_{I/O}$ < 0V .....-50mA
On-State Switch Current, I_T, $V_{I/O}$ = 0V to V_{CC}.....±50mA
Package Thermal Resistance
XTDFN-1×1-6L, θ_{JA}..... 272.8℃/W
XTDFN-1×1-6L, θ_{JB}..... 139.4℃/W
XTDFN-1×1-6L, θ_{JC}..... 158.8℃/W
Junction Temperature.....+150℃
Storage Temperature Range .....-65℃ to +150℃
Lead Temperature (Soldering, 10s).....+260℃
ESD Susceptibility (4) (5)
HBM.....±7000V
CDM .....±1000V

NOTES:
1. All voltages reference ground, unless otherwise noted.
2. If the input and output clamp-current ratings are observed, the input and output negative voltage ratings may be exceeded.
3. The maximum value is 6.5V.
4. For human body model (HBM), all pins comply with ANSI/ESDA/JEDEC JS-001 specifications.
5. For charged device model (CDM), all pins comply with ANSI/ESDA/JEDEC JS-002 specifications.

RECOMMENDED OPERATING CONDITIONS

Supply Voltage, V_{CC}.....1.65V to 5.5V
I/O Port Voltage, $V_{I/O}$ .....0V to V_{CC}
Control Input Voltage, V_{IN}.....0V to 5.5V
Input Transition Rise or Fall Time, Δt/ΔV
V_{CC} = 1.65V to 1.95V..... 20ns/V (MAX)
V_{CC} = 2.3V to 2.7V..... 20ns/V (MAX)
V_{CC} = 3.0V to 3.6V..... 10ns/V (MAX)
V_{CC} = 4.5V to 5.5V..... 10ns/V (MAX)
Operating Temperature Range, T_A..... -40℃ to +125℃

OVERSTRESS CAUTION

Stresses beyond those listed in Absolute Maximum Ratings may cause permanent damage to the device. Exposure to absolute maximum rating conditions for extended periods may affect reliability. Functional operation of the device at any conditions beyond those indicated in the Recommended Operating Conditions section is not implied.

ESD SENSITIVITY CAUTION

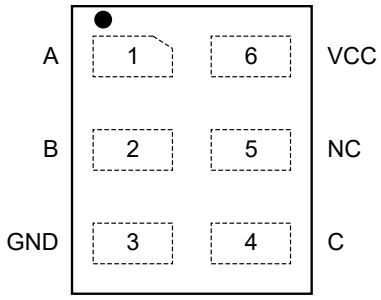
This integrated circuit can be damaged if ESD protections are not considered carefully. SGMICRO recommends that all integrated circuits be handled with appropriate precautions. Failure to observe proper handling and installation procedures can cause damage. ESD damage can range from subtle performance degradation to complete device failure. Precision integrated circuits may be more susceptible to damage because even small parametric changes could cause the device not to meet the published specifications.

DISCLAIMER

SG Micro Corp reserves the right to make any change in circuit design, or specifications without prior notice.

PIN CONFIGURATION

(TOP VIEW)



XTDFN-1×1-6L

PIN DESCRIPTION

PIN	NAME	I/O	FUNCTION
1	A	I/O	Analog Switch A Input/Output Port.
2	B	I/O	Analog Switch B Input/Output Port.
3	GND	—	Ground Pin.
4	C	I	Control Signal Input Port of Switch (L = OFF, H = ON).
5	NC	—	No Connection.
6	VCC	—	Power Supply Input Pin.

FUNCTION TABLE

CONTROL INPUT (C)	SWITCH
L	OFF
H	ON

ELECTRICAL CHARACTERISTICS

(Full = -40℃ to +125℃, typical values are at T_A = +25℃, unless otherwise noted.)

PARAMETER	SYMBOL	CONDITIONS		TEMP	MIN	TYP	MAX	UNITS
Control Input High Voltage	V_{INH}	V_{CC} = 1.65V to 1.95V		Full	0.65 × V_{CC}			V
		V_{CC} = 2.3V to 2.7V		Full	1.6			
		V_{CC} = 3.0V to 3.6V		Full	2.0			
		V_{CC} = 4.5V to 5.5V		Full	0.65 × V_{CC}			
Control Input Low Voltage	V_{INL}	V_{CC} = 1.65V to 1.95V		Full			0.35 × V_{CC}	V
		V_{CC} = 2.3V to 2.7V		Full			0.85	
		V_{CC} = 3.0V to 3.6V		Full			1.1	
		V_{CC} = 4.5V to 5.5V		Full			0.3 × V_{CC}	
On-Resistance	R_{ON}	V_I = V_{CC} to 0V, V_{IN} = V_{IH}, Test Circuit 1	I_S = 4mA, V_{CC} = 1.65V	+25℃		60	135	Ω
				Full			160	
			I_S = 8mA, V_{CC} = 2.3V	+25℃		12	24	
				Full			27	
			I_S = 24mA, V_{CC} = 3.0V	+25℃		7	15	
				Full			18	
			I_S = 32mA, V_{CC} = 4.5V	+25℃		4.5	10	
				Full			13	
On-Resistance Flatness	$R_{FLAT(ON)}$	V_I = V_{CC} to 0V, V_{IN} = V_{IH}, Test Circuit 1	I_S = 4mA, V_{CC} = 1.65V	+25℃		50	120	Ω
				Full			140	
			I_S = 8mA, V_{CC} = 2.3V	+25℃		6	10	
				Full			13	
			I_S = 24mA, V_{CC} = 3.0V	+25℃		2	5	
				Full			7	
			I_S = 32mA, V_{CC} = 4.5V	+25℃		1.2	2	
				Full			3	
Off Leakage Current	$I_{S(OFF)}$	V_{CC} = 5.5V, V_I = V_{CC} and V_O = 0V or V_I = 0V and V_O = V_{CC}, V_{IN} = V_{IL}, Test Circuit 2		Full		±0.05	±1	μA
On Leakage Current	$I_{S(ON)}$	V_{CC} = 5.5V, V_I = V_{CC} or 0V, V_{IN} = V_{IH}, V_O = Open, Test Circuit 3		Full		±0.05	±1	μA
Control Input Current	I_{IN}	V_{CC} = 5.5V, V_{IN} = V_{CC} or 0V		Full		±0.05	±1	μA
Supply Current	I_{CC}	V_{CC} = 5.5V, V_{IN} = V_{CC} or 0V		Full		0.05	1	μA
Supply Current Change	ΔI_{CC}	V_{CC} = 5.5V, V_{IN} = V_{CC} - 0.6V		Full		0.05	10	μA
Control Input Capacitance	C_{INC}	V_{CC} = 5.0V, f = 1MHz		+25℃		3		pF
Switch Input/Output Capacitance	$C_{IO(OFF)}$	V_{CC} = 5.0V, f = 1MHz		+25℃		6		pF
Switch Input/Output Capacitance	$C_{IO(ON)}$	V_{CC} = 5.0V, f = 1MHz		+25℃		13		pF

ANALOG SWITCH CHARACTERISTICS

(T_A = +25℃, unless otherwise noted.)

PARAMETER	FROM (INPUT)	TO (OUTPUT)	CONDITIONS		TEMP	MIN	TYP	MAX	UNITS
Frequency Response (Switch On)	A or B	B or A	C_L = 20pF, R_L = 600Ω, f = sine wave, Test Circuit 4	V_{CC} = 1.65V	+25℃		90		MHz
				V_{CC} = 2.3V	+25℃		170		
				V_{CC} = 3.0V	+25℃		190		
				V_{CC} = 4.5V	+25℃		205		
			C_L = 5pF, R_L = 50Ω, f = sine wave, Test Circuit 4	V_{CC} = 1.65V	+25℃		310		
				V_{CC} = 2.3V	+25℃		320		
				V_{CC} = 3.0V	+25℃		325		
				V_{CC} = 4.5V	+25℃		335		
Crosstalk (Control Input to Signal Output)	C	A or B	C_L = 50pF, R_L = 600Ω, f = 1MHz (square wave), Test Circuit 5	V_{CC} = 1.65V	+25℃		35		mV
				V_{CC} = 2.3V	+25℃		55		
				V_{CC} = 3.0V	+25℃		85		
				V_{CC} = 4.5V	+25℃		140		
Feedthrough Attenuation (Switch Off)	A or B	B or A	C_L = 50pF, R_L = 600Ω, f = 1MHz (sine wave), Test Circuit 6	V_{CC} = 1.65V	+25℃		-50		dB
				V_{CC} = 2.3V	+25℃		-50		
				V_{CC} = 3.0V	+25℃		-50		
				V_{CC} = 4.5V	+25℃		-50		
			C_L = 5pF, R_L = 50Ω, f = 1MHz (sine wave), Test Circuit 6	V_{CC} = 1.65V	+25℃		-75		
				V_{CC} = 2.3V	+25℃		-75		
				V_{CC} = 3.0V	+25℃		-75		
				V_{CC} = 4.5V	+25℃		-75		
Sine-Wave Distortion	A or B	B or A	C_L = 50pF, R_L = 10kΩ (tied to V_{CC}/2), f = 1kHz (sine wave), Test Circuit 7	V_{CC} = 1.65V	+25℃		0.065		%
				V_{CC} = 2.3V	+25℃		0.015		
				V_{CC} = 3.0V	+25℃		0.005		
				V_{CC} = 4.5V	+25℃		0.002		
			C_L = 50pF, R_L = 10kΩ (tied to V_{CC}/2), f = 10kHz (sine wave), Test Circuit 7	V_{CC} = 1.65V	+25℃		0.07		
				V_{CC} = 2.3V	+25℃		0.015		
				V_{CC} = 3.0V	+25℃		0.005		
				V_{CC} = 4.5V	+25℃		0.003		

SWITCHING CHARACTERISTICS

(Full = -40℃ to +125℃, typical values are at T_A = +25℃, unless otherwise noted.)

PARAMETER	SYMBOL	FROM (INPUT)	TO (OUTPUT)	CONDITIONS	TEMP	MIN (1)	TYP	MAX (1)	UNITS
Propagation Delay Time	t_{PD}	A or B	B or A	V_{CC} = 1.8V ± 0.15V	Full	0.01	0.45	2	ns
				V_{CC} = 2.5V ± 0.2V	Full	0.05	0.35	1.5	
				V_{CC} = 3.3V ± 0.3V	Full	0.05	0.65	1.5	
				V_{CC} = 5.0V ± 0.5V	Full	0.05	0.6	1.5	
Enable Time	t_{EN}	C	A or B	V_{CC} = 1.8V ± 0.15V	Full	1	7.4	12	ns
				V_{CC} = 2.5V ± 0.2V	Full	1	3.9	6.5	
				V_{CC} = 3.3V ± 0.3V	Full	1	3	5.5	
				V_{CC} = 5.0V ± 0.5V	Full	1	2.7	4.5	
Disable Time	t_{DIS}	C	A or B	V_{CC} = 1.8V ± 0.15V	Full	1	7.3	11	ns
				V_{CC} = 2.5V ± 0.2V	Full	1	4	6.5	
				V_{CC} = 3.3V ± 0.3V	Full	1	3.5	6	
				V_{CC} = 5.0V ± 0.5V	Full	1	3	6	

NOTE:

1. Specified by design and characterization, not production tested.

OPERATING CHARACTERISTICS

(T_A = +25℃, unless otherwise noted.)

PARAMETER	SYMBOL	CONDITIONS	TEMP	V_{CC} = 1.8V	V_{CC} = 2.5V	V_{CC} = 3.3V	V_{CC} = 5.0V	UNITS
				TYP	TYP	TYP	TYP	
Power Dissipation Capacitance	C_{PD} (1)	f = 10MHz	+25℃	6	6.2	6.5	7	pF

NOTE:

1. C_{PD} is used to determine the dynamic power dissipation (P_D in μW).

$P_D = C_{PD} \times V_{CC}^2 \times f \times N + \{(C_L + C_S) \times V_{CC} \times f_O\}$

where:

f = input frequency in MHz.

f_O = output frequency in MHz.

C_L = output load capacitance in pF.

C_S = switch capacitance in pF.

V_{CC} = supply voltage in V.

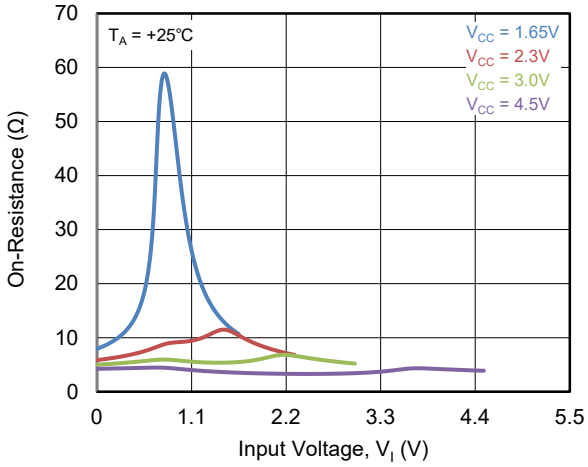
N = total load switching outputs.

The condition is V_I = 0V to V_{CC}.

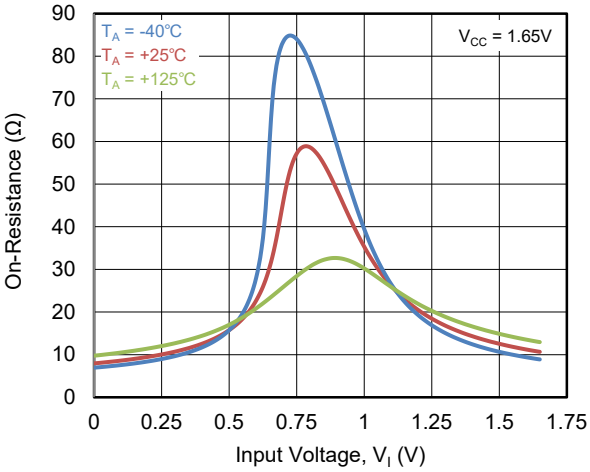
TYPICAL PERFORMANCE CHARACTERISTICS

T_A = +25℃, V_{CC} = 3.0V, unless otherwise noted.

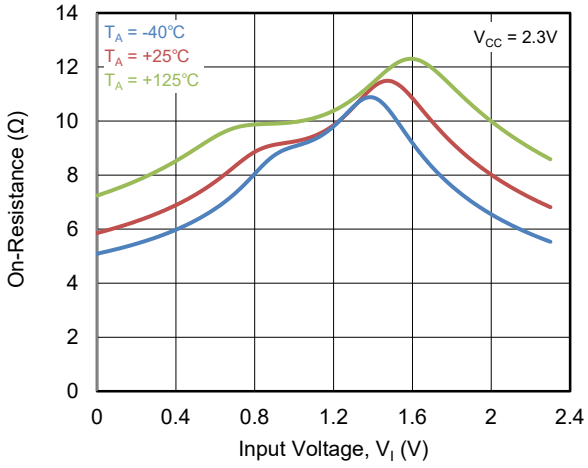
On-Resistance vs. Input Voltage, V_I



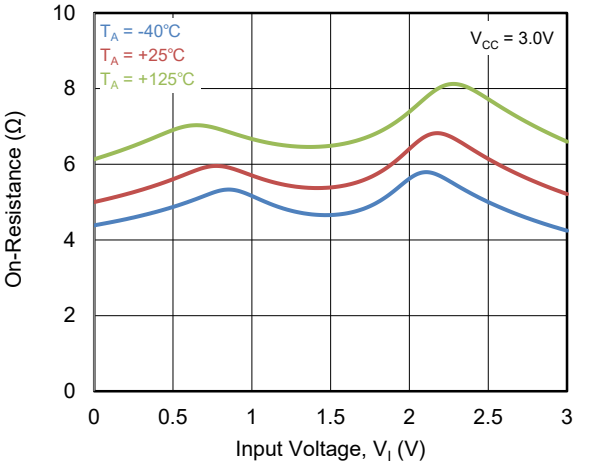
On-Resistance vs. Input Voltage, V_I



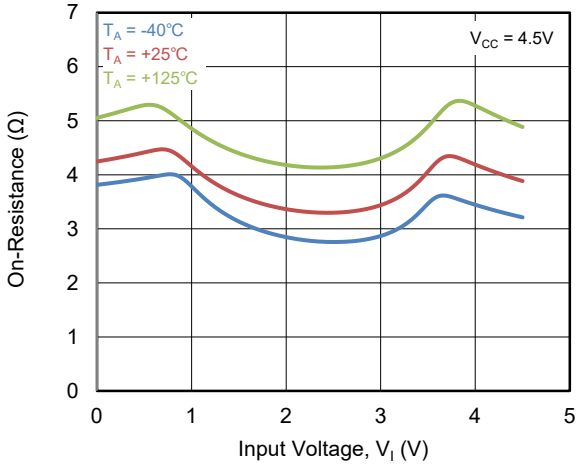
On-Resistance vs. Input Voltage, V_I



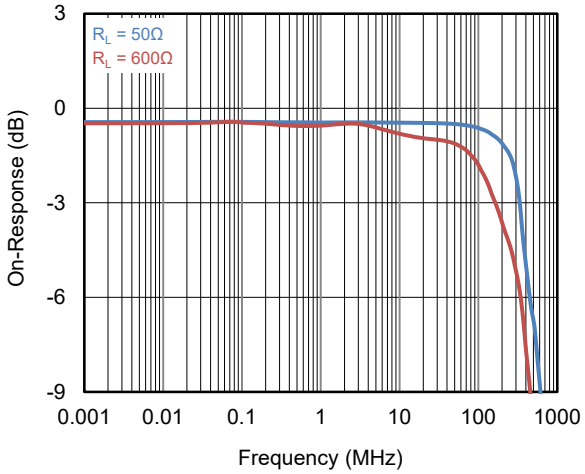
On-Resistance vs. Input Voltage, V_I



On-Resistance vs. Input Voltage, V_I

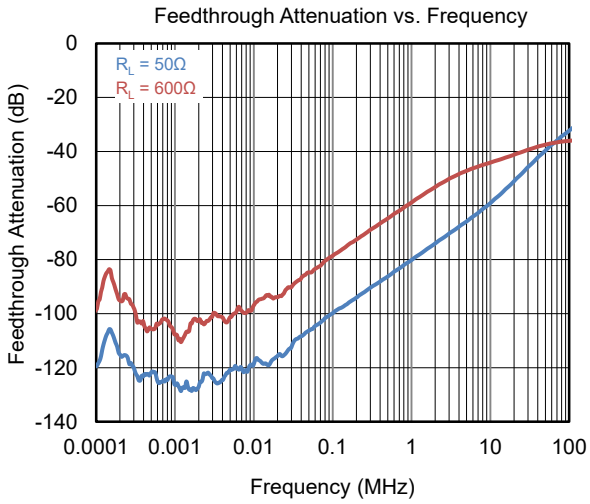


On-Response vs. Frequency



TYPICAL PERFORMANCE CHARACTERISTICS (continued)

T_A = +25℃, V_{CC} = 3.0V, unless otherwise noted.



TEST CIRCUITS

Table 1. Parameter Test Information

PARAMETER	V_{CC}	S1	INPUTS			V_M	C_L	R_L	V_Δ
			V_I	V_{IN}	t_R/t_F				
t_{PLH}/t_{PHL}	1.8V ± 0.15V	Open	0V to V_{CC}	V_{CC}	≤ 2ns	$V_{CC}/2$	30pF	1kΩ	-
	2.5V ± 0.2V	Open	0V to V_{CC}	V_{CC}	≤ 2ns	$V_{CC}/2$	30pF	500Ω	-
	3.3V ± 0.3V	Open	0V to V_{CC}	V_{CC}	≤ 2.5ns	$V_{CC}/2$	50pF	500Ω	-
	5.0V ± 0.5V	Open	0V to V_{CC}	V_{CC}	≤ 2.5ns	$V_{CC}/2$	50pF	500Ω	-
t_{PLZ}/t_{PZL}	1.8V ± 0.15V	2 × V_{CC}	GND	0V to V_{CC}	≤ 2ns	$V_{CC}/2$	30pF	1kΩ	0.15V
	2.5V ± 0.2V	2 × V_{CC}	GND	0V to V_{CC}	≤ 2ns	$V_{CC}/2$	30pF	500Ω	0.15V
	3.3V ± 0.3V	2 × V_{CC}	GND	0V to V_{CC}	≤ 2.5ns	$V_{CC}/2$	50pF	500Ω	0.3V
	5.0V ± 0.5V	2 × V_{CC}	GND	0V to V_{CC}	≤ 2.5ns	$V_{CC}/2$	50pF	500Ω	0.3V
t_{PHZ}/t_{PZH}	1.8V ± 0.15V	GND	V_{CC}	0V to V_{CC}	≤ 2ns	$V_{CC}/2$	30pF	1kΩ	0.15V
	2.5V ± 0.2V	GND	V_{CC}	0V to V_{CC}	≤ 2ns	$V_{CC}/2$	30pF	500Ω	0.15V
	3.3V ± 0.3V	GND	V_{CC}	0V to V_{CC}	≤ 2.5ns	$V_{CC}/2$	50pF	500Ω	0.3V
	5.0V ± 0.5V	GND	V_{CC}	0V to V_{CC}	≤ 2.5ns	$V_{CC}/2$	50pF	500Ω	0.3V

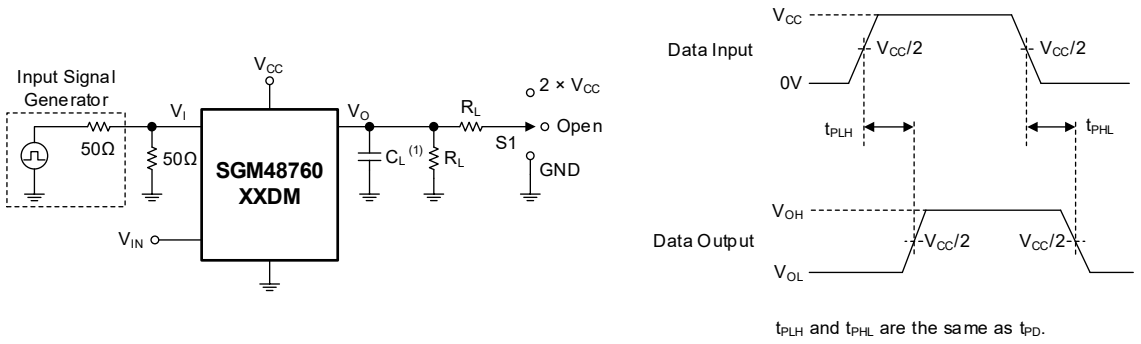


Figure 2. Voltage Waveforms & Propagation Delay Time (t_{PD})

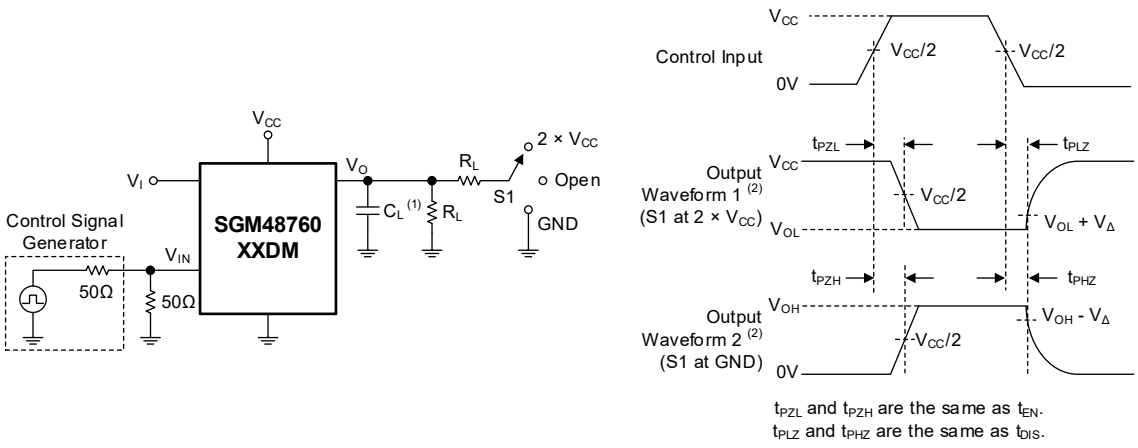
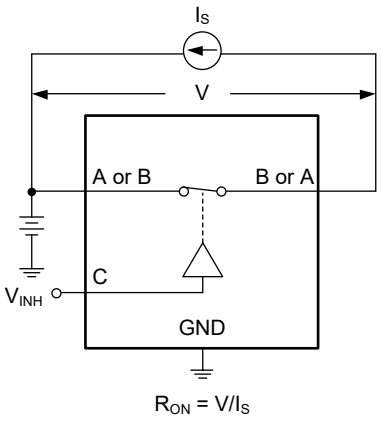


Figure 3. Voltage Waveforms & Enable and Disable Times (t_{EN}, t_{DIS})

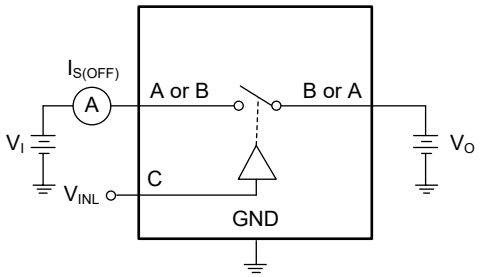
NOTES:

1. The C_L includes probe capacitance and clamp capacitance.
2. Waveform 1 indicates the output when internal conditions force the output to low, except the output port is disabled through the output control. Waveform 2 indicates the output when internal conditions force the output to high, except the output port is disabled through the output control.
3. For all input signals from signal generator equipment, the following conditions are required: PRR ≤ 10MHz, Z_O = 50Ω.
4. Only one output port is measured with one transition at a time.

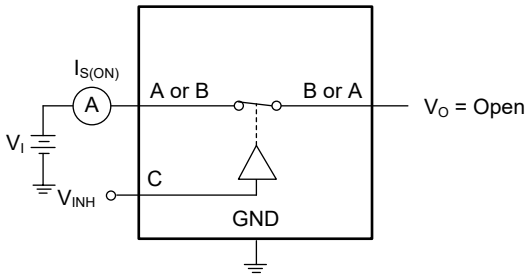
TEST CIRCUITS (continued)



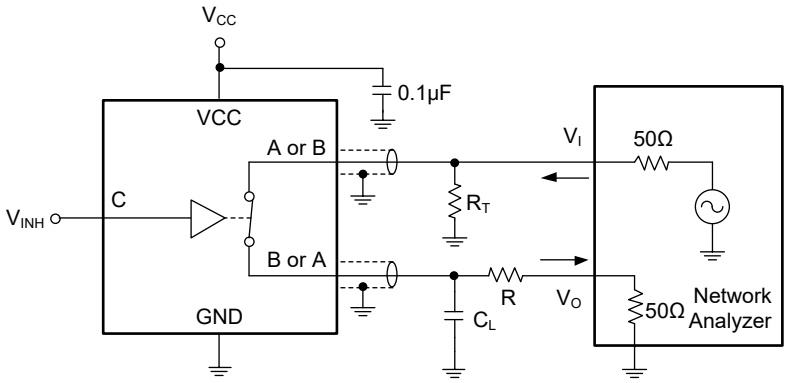
Test Circuit 1. On-Resistance



Test Circuit 2. Off Leakage Current



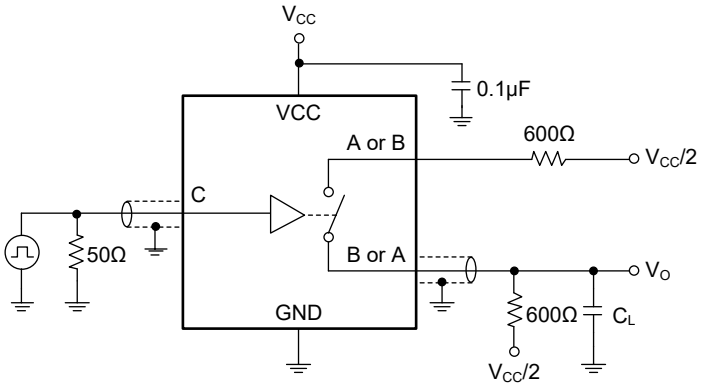
Test Circuit 3. On Leakage Current



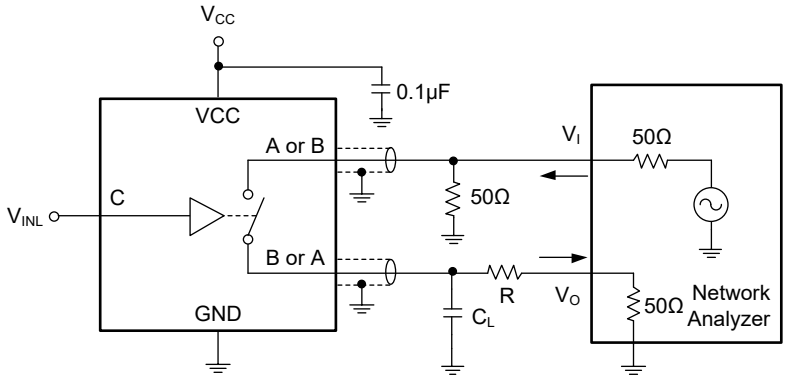
R_L = 600Ω: R = 550Ω, R_T = 50Ω
R_L = 50Ω: R = 0Ω, R_T = Open

Test Circuit 4. Frequency Response (Switch On)

TEST CIRCUITS (continued)

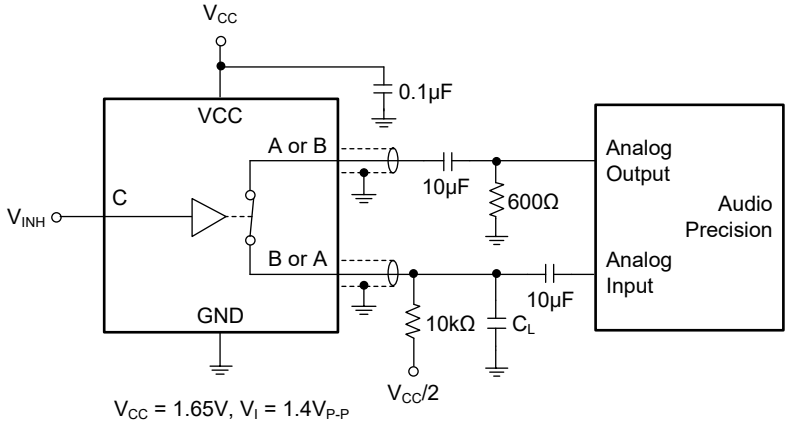


Test Circuit 5. Crosstalk (Control Input to Signal Output)



R_L = 600Ω: R = 550Ω
R_L = 50Ω: R = 0Ω

Test Circuit 6. Feedthrough Attenuation (Switch Off)



V_{CC} = 1.65V, V_I = 1.4V_{P-P}
V_{CC} = 2.3V, V_I = 2V_{P-P}
V_{CC} = 3.0V, V_I = 2.5V_{P-P}
V_{CC} = 4.5V, V_I = 4V_{P-P}

Test Circuit 7. Sine-Wave Distortion

DETAILED DESCRIPTION

The SGM48760XXDM operates power supply voltage range from 1.65V to 5.5V and it can support up to 5.5V bidirectional signal transmission. When the input control pin (C) accepts a high-level voltage, the analog switch is turned on and the signal transmission is started. When the input control pin (C) accepts a low-level voltage, the analog switch is turned off and the signal transmission is prohibited.

Feature Description

The SGM48760XXDM has low on-resistance of 4.5Ω (TYP) at V_{CC} = 4.5V and low t_{PD} of 0.65ns (TYP) at V_{CC} = 3.3V, so that it can be applied to high-speed signal transmission. Besides, its quick enable and disable times make this device suitable for high-speed signal switching applications. In particular, the control pin can accept a higher voltage than V_{CC} because the SGM48760XXDM has no equivalent diodes between the control pin and VCC pin. The maximum input voltage of control pin can reach to 5.5V when V_{CC} is 0V. This feature allows the SGM48760XXDM to be used in applications where the control pin voltage is higher than the power supply.

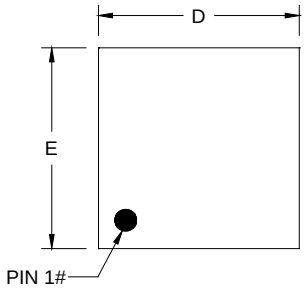
REVISION HISTORY

NOTE: Page numbers for previous revisions may differ from page numbers in the current version.

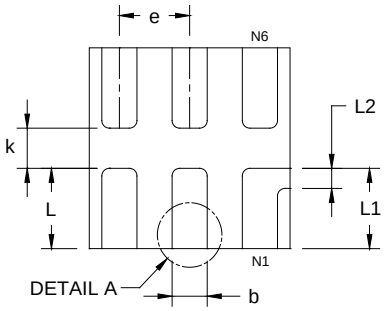
Changes from Original to REV.A (OCTOBER 2026)	Page
Changed from product preview to production data	All

PACKAGE OUTLINE DIMENSIONS

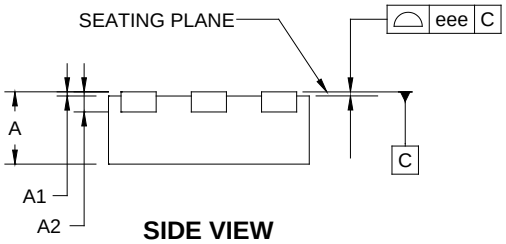
XTDFN-1×1-6L



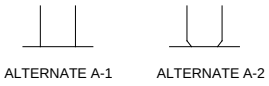
TOP VIEW



BOTTOM VIEW

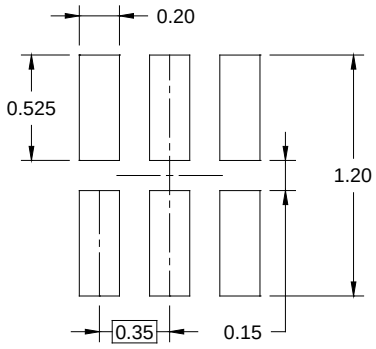


SIDE VIEW



DETAIL A

ALTERNATE TERMINAL CONSTRUCTION



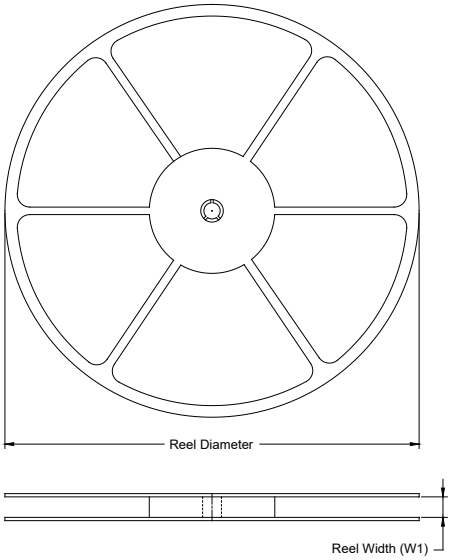
RECOMMENDED LAND PATTERN (Unit: mm)

Symbol	Dimensions In Millimeters		
	MIN	NOM	MAX
A	0.320	-	0.400
A1	0.000	-	0.050
A2	0.100 REF		
b	0.120	-	0.230
D	0.950	-	1.050
E	0.950	-	1.050
e	0.350 BSC		
k	0.150 MIN		
L	0.350	-	0.450
L1	0.350	-	0.450
L2	0.100 REF		
eee	0.050		

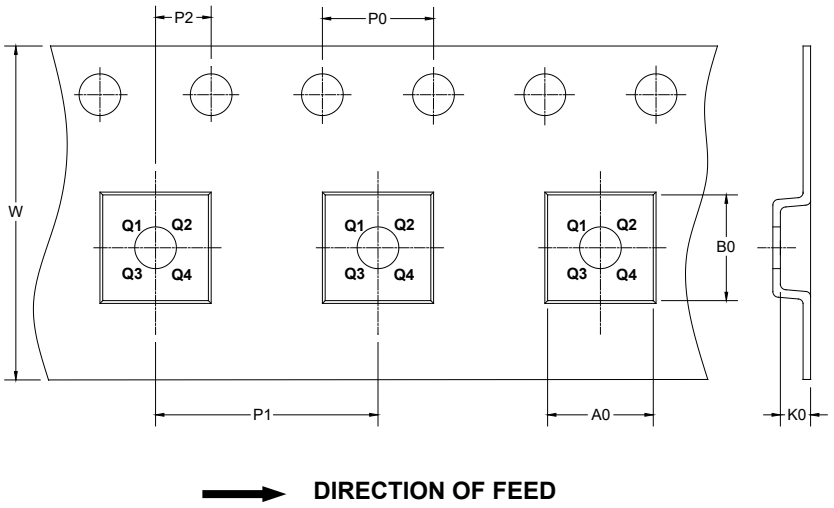
NOTE: This drawing is subject to change without notice.

TAPE AND REEL INFORMATION

REEL DIMENSIONS



TAPE DIMENSIONS



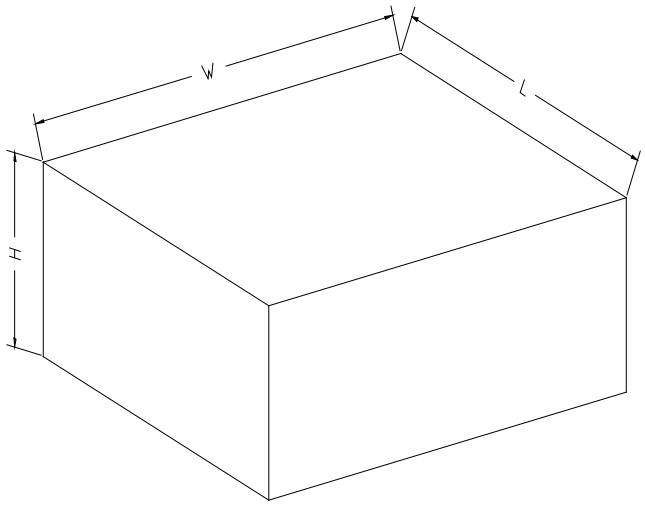
NOTE: The picture is only for reference. Please make the object as the standard.

KEY PARAMETER LIST OF TAPE AND REEL

Package Type	Reel Diameter	Reel Width W1 (mm)	A0 (mm)	B0 (mm)	K0 (mm)	P0 (mm)	P1 (mm)	P2 (mm)	W (mm)	Pin1 Quadrant
XTDFN-1×1-6L	7"	9.5	1.15	1.17	0.56	4.0	4.0	2.0	8.0	Q3

DD0001

CARTON BOX DIMENSIONS



NOTE: The picture is only for reference. Please make the object as the standard.

KEY PARAMETER LIST OF CARTON BOX

Reel Type	Length (mm)	Width (mm)	Height (mm)	Pizza/Carton
7″ (Option)	368	227	224	8
7″	442	410	224	18

DD0002